

Designing HPC Architectures at the Barcelona Supercomputing Center

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Resumen:

The European Commission recently announced the creation of the European Processor Initiative (EPI), a European consortium to co-design, develop and bring to the market a European low-power microprocessor. EPI will start in 2018 and will develop the first European High Performance Computing (HPC) Systems on Chip (SoC) and accelerators. Both elements will be implemented and validated in a prototype system that will become the basis for a full Exascale machine based on European technology

Sobre Miquel Moretó:

Miquel Moreto is a Ramon y Cajal researcher at UPC and associate researcher at the Barcelona Supercomputing Center (BSC), Spain. He received the BSc and MSc degrees in mathematics and telecommunications engineering from UPC, and the PhD degree in 2010 in the Computer Architecture Department at the same university. He was a Fulbright postdoctoral fellow at the International Computer Science Institute (ICSI), affiliated with UC Berkeley, USA, from 2012 to 2013. In 2013, he joined the BSC to work on the ERC-funded project RoMoL and the Mont-Blanc European project. His research interests include HPC architectures and hardware-software co-design for future massively parallel systems.